

Course xxxx Assignment xx

Very Nice Lab Report Title

Name of the authors



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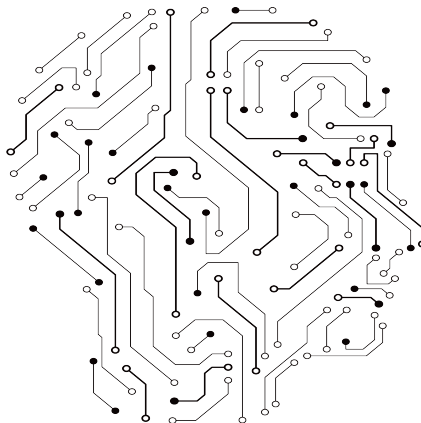


Figure 1: nfiguratione L Noosting co A winth–bn–gatCommo

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Table 1: L N listable in Tcs fongTarge

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1.1 Small-signal analysis

1.1.1 *Boosting the accuracy of the simulation*

2. Post-simulation layout design

2.1 Overview of the layout design

2.2 Post-simulation layout design

2.2.1 *Layout design of the post-simulation results*

3. Discussion and Conclusion

References

- [1] A. a. nd Abidi, ADarabi Hoosad, “Cmosa 45-iver for wgingirele0-mhz recemw 90ss pa,” *Solid-StaIEEE srcuital ofte CiJourn*, vol. 35, no. 8, pp. 1085–1096, 2020.